

# WEST Search History





DATE: Thursday, December 01, 2005

| Hide?                    | <u>Set</u><br><u>Name</u>            | <u>Query</u>  | <u>Hit</u><br><u>Count</u> |
|--------------------------|--------------------------------------|---|----------------------------|
|                          | <i>DB=PGPB,USPT; PLUR=YES; OP=OR</i> |   |                            |
| <input type="checkbox"/> | L83                                  | LIFO near implement\$5  | 56                         |
| <input type="checkbox"/> | L82                                  | L81 and @ad<20021011  | 19                         |
| <input type="checkbox"/> | L81                                  | (bypass near (multiplexor or multiplexer)) same RAM   | 21                         |
| <input type="checkbox"/> | L80                                  | L79 and 711   | 50                         |
| <input type="checkbox"/> | L79                                  | bypass adj (multiplexer or multiplexor)   | 377                        |
| <input type="checkbox"/> | L78                                  | L77 and LIFO  | 5                          |
| <input type="checkbox"/> | L77                                  | bypass adj multiplex\$2   | 294                        |
| <input type="checkbox"/> | L76                                  | L75 and 711   | 50                         |
| <input type="checkbox"/> | L75                                  | L73 and (stack or queue or LIFO or FIFO)  | 259                        |
| <input type="checkbox"/> | L74                                  | L73 and 711   | 70                         |
| <input type="checkbox"/> | L73                                  | multiplex\$2 near output near (register or latch or memory or sampl\$3)                                   | 674                        |
| <input type="checkbox"/> | L72                                  | L71 and ((single\$1port or one\$1port) adj RAM)   | 36                         |
| <input type="checkbox"/> | L71                                  | L18 and (stack or queue or POP or PUSH)   | 1497                       |
| <input type="checkbox"/> | L70                                  | L69 and ((one\$1port or single\$1port) adj RAM)   | 2                          |
| <input type="checkbox"/> | L69                                  | L68 and (selector or multiplexer)   | 298                        |
| <input type="checkbox"/> | L68                                  | L67 and L22   | 767                        |
| <input type="checkbox"/> | L67                                  | stack or queue  | 278804                     |
| <input type="checkbox"/> | L66                                  | L65 and @ad<=20021011   | 91                         |
| <input type="checkbox"/> | L65                                  | L60 and (LIFO or (Last adj in\$first adj out))  | 98                         |
| <input type="checkbox"/> | L64                                  | L60 and ((multipl\$5 or two or (more adj than adj one)) adj RAM)  | 152                        |
| <input type="checkbox"/> | L63                                  | L60 and ((multipl\$5 or two or (more adj than adj one)) adj RAM)  | 152                        |
| <input type="checkbox"/> | L62                                  | L61 and L41 and L45   | 9                          |
| <input type="checkbox"/> | L61                                  | L60 and L52   | 3106                       |
| <input type="checkbox"/> | L60                                  | 365/189.04 365/230.04 365/239 365/219 365/189.02 365/221 365/230.04<br>365/230.03 365/236 714/743 327/427 | 12551                      |
| <input type="checkbox"/> | L59                                  | 365/189.04 365/230.04 365/239 365/219 365/189.02 365/221 365/230.04<br>365/230.03 365/236 714/743         | 11361                      |
| <input type="checkbox"/> | L58                                  | 365/189.04  | 2193                       |
| <input type="checkbox"/> | L57                                  | 5206834.pn. 5287309.pn. 20010049807 20040117542   | 4                          |
| <input type="checkbox"/> | L56                                  | 5206834.pn. 5287309.pn. 20010049807   | 3                          |
| <input type="checkbox"/> | L55                                  | L54 and sampl\$3  | 541                        |
| <input type="checkbox"/> | L54                                  | L53 and (multi\$1plex\$3)   | 1590                       |
| <input type="checkbox"/> | L53                                  | ((multipl\$ or two or "more than one") adj (RAM or memory)) and 711                                       | 3098                       |
| <input type="checkbox"/> | L52                                  | (multipl\$ or two or "more than one") adj (RAM or memory)   | 28985                      |
| <input type="checkbox"/> | L51                                  | L50 and (not 711)   | 9                          |

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|  |     |   |        |
|--|-----|---|--------|
| <input type="checkbox"/>                                 | L50 | L49 and L46   | 17     |
| <input type="checkbox"/>                                 | L49 | 710 711 712   | 231916 |
| <input type="checkbox"/>                                 | L48 | 711 or 707 or 710   | 245575 |
| <input type="checkbox"/>                                 | L47 | L46 and 711   | 8      |
| <input type="checkbox"/>                                 | L46 | reverse adj (queue or stack)  | 81     |
| <input type="checkbox"/>                                 | L45 | L44 and (not optical) and @ad<=20021011                             | 93     |
| <input type="checkbox"/>                                 | L44 | L43 and RAM   | 182    |
| <input type="checkbox"/>                                 | L43 | L42 and (revers\$ or backwards or invert or inverse)                | 266    |
| <input type="checkbox"/>                                 | L42 | L41 and 711   | 688    |
| <input type="checkbox"/>                                 | L41 | multi\$port   | 10197  |
| <input type="checkbox"/>                                 | L40 | L38 and L32   | 8      |
| <input type="checkbox"/>                                 | L39 | L38 L32   | 2244   |
| <input type="checkbox"/>                                 | L38 | 711/168   | 1009   |
| <input type="checkbox"/>                                 | L37 | L36 and L35   | 8      |
| <input type="checkbox"/>                                 | L36 | L33 and RAM   | 8      |
| <input type="checkbox"/>                                 | L35 | (reverse adj stack) and RAM   | 8      |
| <input type="checkbox"/>                                 | L34 | (reverse adj stack) and (dual\$port adj ram)                        | 0      |
| <input type="checkbox"/>                                 | L33 | reverse adj stack   | 66     |
| <input type="checkbox"/>                                 | L32 | dual\$port adj ram  | 1243   |
| <input type="checkbox"/>                                 | L31 | 6275898.pn. 20010054133 20040044854 20040098394                     | 4      |
| <input type="checkbox"/>                                 | L30 | L29 and 711   | 7      |
| <input type="checkbox"/>                                 | L29 | L28 and L24   | 40     |
| <input type="checkbox"/>                                 | L28 | (backward\$1 or reverse or invert\$) adj (chronological or order\$) | 30214  |
| <input type="checkbox"/>                                 | L27 | reverse adj chronological   | 555    |
| <input type="checkbox"/>                                 | L26 | reverse adj order\$   | 27425  |
| <input type="checkbox"/>                                 | L25 | L22 and L24   | 27     |
| <input type="checkbox"/>                                 | L24 | (single\$port adj RAM) or (dual\$port adj RAM)                      | 1302   |
| <input type="checkbox"/>                                 | L23 | L22 and L21   | 11     |
| <input type="checkbox"/>                                 | L22 | 711/104 711/167   | 3148   |
| <input type="checkbox"/>                                 | L21 | L20 and @ad<=20021011   | 89     |
| <input type="checkbox"/>                                 | L20 | L19 and RAM   | 99     |
| <input type="checkbox"/>                                 | L19 | L18 and 711   | 201    |
| <input type="checkbox"/>                                 | L18 | LIFO  | 1996   |
| <input type="checkbox"/>                                 | L17 | 20040117542   | 1      |
| <i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI; PLUR=YES; OP=OR</i> |     |   |        |
| <input type="checkbox"/>                                 | L16 | L15 same L2   | 9      |
| <input type="checkbox"/>                                 | L15 | lifo or ("last-in")   | 3282   |
| <input type="checkbox"/>                                 | L14 | (711/157).ccls. and L11   | 4      |
| <input type="checkbox"/>                                 | L13 | (711/5).ccls. and L11   | 6      |
| <input type="checkbox"/>                                 | L12 | "711"/\$.ccls. and L11  | 108    |
| <input type="checkbox"/>                                 | L11 | L10 same L2   | 672    |
| <input type="checkbox"/>                                 | L10 | ram or dram or sdram or (random adj access\$3 adj memory)           | 661059 |

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|--------------------------|----|---|-------|
| <input type="checkbox"/> | L9 | "365"/\$.ccls. and L3                               | 23    |
| <input type="checkbox"/> | L8 | "365"/\$.ccls. and L2                               | 1004  |
| <input type="checkbox"/> | L7 | "711"/\$.ccls. and L2                               | 928   |
| <input type="checkbox"/> | L6 | "711"/\$.ccls. and L3                               | 17    |
| <input type="checkbox"/> | L5 | L1 near3 sampl\$3                                   | 1016  |
| <input type="checkbox"/> | L4 | L1 near3 (register of latch or buffer)              | 7075  |
| <input type="checkbox"/> | L3 | L1 near5 sampl\$3                                   | 1522  |
| <input type="checkbox"/> | L2 | L1 near5 (register or latch or buffer)              | 10071 |
| <input type="checkbox"/> | L1 | (multiplex\$3 or multi-plex\$3 or mux) near2 output | 60744 |

END OF SEARCH HISTORY

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